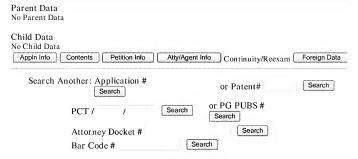
Continuity/Reexam Information for 10/022659

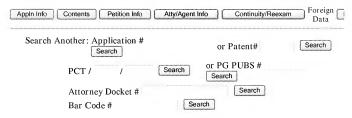


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No Foreign Data



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Application Number Information

Application Number: 10/022659 Examiner Number: 80488 / TORRES, JUAN

Assignments

Filing or 371(c) Date: 12/18/2001 eDan Group Art Unit: 2611 IFW Madras

Effective Date: 12/18/2001 Class/Subclass: 375/265 000

Application Received: 12/20/2001 Lost Case: NO Waiting for Response

Pat. Num./Pub. Num: /20030112885 Appeal Number: 20083242 Desc.
Mail APDP

Issue Date: 00/00/0000 Unmatched Petition: NO

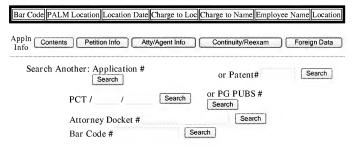
Date of Abandonment: 00/00/0000 L&R Code: Secrecy Code:1

Attorney Docket Number: 14-6 Third Level Review: NO Secrecy Order: NO
Status: 135 /BOARD OF APPEALS DECISION RENDERED Status Date: 10/20/2008

Confirmation Number: 1760 Oral Hearing: NO

Title of Invention: METHOD AND APPARATUS FOR JOINT EQUALIZATION AND

DECODING OF MULTILEVEL CODES



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Application Submit

IDS Flag Clearance for Application 10022659

IDS Information

Content	Mailroom Date	Entry Number	iDS Review	Last Modified	Reviewer
WIDS	2001-12-18	5	Y 🗹	2007-05-09 00:00:00.0	CR #232884
Update					

Inventor Information for 10/022659

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HARATSCH, ERICH FRANZ	BRADLEY BEACH	NEW JERSEY
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Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name
11141337	7355451	20060017468	150	05/31/2005	COMMON-MODE SHIFTING CIRCUIT FOR CML BUFFERS	LE,DON
12020722	Not Issued	20080117805	030	01/28/2008	METHOD AND APPARATUS FOR CROSS-TALK CANCELLATION IN FREQUENCY DIVISION MULTIPLEXED TRANSMISSION SYSTEMS	TROST IV,WILLIAM
12039474	Not Issued	0	020	02/28/2008	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	
60366923	Not Issued		159	03/22/2002	BANDWIDTH EFFICIENT MULTICARRIER TRANSMISSION SYSTEM WITH CROSS-TALK CANCELLATION	
10106990	6771198	20030132870	150		METHOD AND APPARATUS FOR CONVERTING BETWEEN ANALOG AND DIGITAL DOMAINS USING FREQUENCY INTERLEAVING	YOUNG,BRIAN

60387828	Not Issued		159	06/10/2002	DENSE WAVELENGTH DIVISION MULTIPLEXING (DWDM) SINGLE SIDEBAND (SSB) OPTICAL TRANSMISSION SCHEME BASED ON OPTICAL MUX/DEMUX	
10348871	7164711	20040141552	150	01/22/2003	PROGRAMMABLE RECEIVE-SIDE CHANNEL EQUALIZER	VO,DON
10378096	7257329	20040057733	150	02/28/2003	DUOBINARY PULSE SHAPING FOR OPTICAL TRANSMISSION SYSTEMS EMPLOYING PULSE AMPLITUDE MODULATION TECHNIQUES	LI,SHI
10610334	Not Issued	20040146002	041	06/30/2003	METHOD AND APPARATUS FOR REDUCING CROSS- TALK WITH REDUCED REDUNDANCIES	CORRIELUS,JEA
10610335	Not Issued	20050018777	120	06/30/2003	METHOD AND APPARATUS FOR REDUCING NOISE IN AN UNBALANCED CHANNEL USING COMMON MODE COMPONENT	PERILLA,JASON
10610336	7263541	20040148323		06/30/2003	MULTI- DIMENSIONAL HYBRID AND TRANSPOSE FORM FINITE IMPULSE RESPONSE FILTERS	MAI,TAN
11191895	Not Issued	20070025490	071	07/28/2005	DIGITAL PHASE- LOCKED LOOP	MCKIE,GINA
11234446	7363576	20060020877	150	09/26/2005	METHOD AND APPARATUS FOR PIPELINED JOINT	TORRES,JOSEPF

					EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	
11256182	Not Issued	20060039492	077	10/21/2005	HYBRID MEMORY ARCHITECTURE FOR REDUCED STATE SEQUENCE ESTIMATION (RSSE) TECHNIQUES	PHU,PHUONG
11781313	Not Issued	20070266069	030	07/23/2007	MULTI- DIMENSIONAL HYBRID AND TRANSPOSE FORM FINITE IMPULSE RESPONSE FILTERS	BULLOCK JR,LEWIS
11859437	Not Issued	0	030	09/21/2007	METHODS AND APPARATUS FOR PERFORMING REDUCED COMPLEXITY DISCRETE FOURIER TRANSFORMS USING INTERPOLATION	BULLOCK JR,LEWIS
60180079	Not Issued		159	02/03/2000	ZERO-OVERHEAD CODING FOR LAN SERIAL AND WDM SOLUTIONS	
60245519	Not Issued		159	11/03/2000	PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	
60347462	Not Issued		159	01/11/2002	FREQUENCY INTERLEAVED ANALOG TO DIGITAL CONVERSION	
60366930	Not Issued		159	03/22/2002	OPTO- ELECTRONICALLY CALIBRATED PAM TRANSCEIVER	
60525229	Not Issued		159	11/25/2003	UNIVERSAL CONTROLLER FOR PC PERIPHERALS	
60525230	Not Issued		159	11/25/2003	LOW PIN-COUNT CONTROLLER WITH SHARED EEPROM	

60525231	Not Issued		159	11/25/2003	SOFTWARE- DIRECTED POWER MANAGEMENT FOR ETHERNET NETWORK DEVICE	
11359201		20060143259		02/22/2006	LOW POWER VECTOR SUMMATION APPARATUS	MAI,TAN
09834668	7000175	20020083396	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	TORRES, JOSEPI
10022659	Not Issued	20030112885	135	12/18/2001	METHOD AND APPARATUS FOR JOINT EQUALIZATION AND DECODING OF MULTILEVEL CODES	TORRES,JUAN
10022665	Not Issued	20030115541	124	12/18/2001	METHOD AND APPARATUS FOR JOINT EQUALIZATION AND DECODING OF MULTIDIMENSIONAL CODES TRANSMITTED OVER MULTIPLE SYMBOL DURATIONS	TORRES,JOSEPH
10122997	7085794	20030195906	150	04/12/2002	LOW POWER VECTOR SUMMATION METHOD AND APPARATUS	MAI,TAN
10219905	Not Issued	20030180055	120	08/15/2002	OPTICALLY CALIBRATED PULSE AMPLITUDE MODULATED TRANSMISSION SCHEME FOR OPTICAL CHANNELS	LI,SHI
10219904	7155134	20030180041	150		PULSE AMPLITUDE MODULATED	PHAN,HANH

					TRANSMISSION SCHEME FOR OPTICAL CHANNELS WITH SOFT DECISION DECODING	
10229623	Not Issued	20040208583	161	08/28/2002	SINGLE SIDEBAND DENSE WAVELENGTH DIVISION MULTIPLEXED OPTICAL TRANSMISSION SCHEME	PHAN,HANH
10219906	7362719	20030179766	150	08/15/2002	METHOD AND APPARATUS FOR CROSS-TALK CANCELLATION IN FREQUENCY DIVISION MULTIPLEXED TRANSMISSION SYSTEMS	SAM,PHIRIN
60412870	Not Issued		159	09/23/2002	DUOBINARY PULSE SHAPING FOR PAM TRANSMISSION OVER OPTICAL CHANNELS	
60443261	Not Issued		159	01/28/2003	CROSS-TALK CANCELLER AND EQUALIZER ARCHITECTURE FOR UNBALANCED CHANNELS	
60443258	Not Issued		159	01/28/2003	MULTI- DIMENSIONAL HYBRID-FORM FIR FILTER ARCHITECTURE	
60443260	Not Issued		159	01/28/2003	ZERO-REDUNDANCY CROSS-TALK CANCELLATION ARCHITECTURE	
10874834	Not Issued	20050114721	061	06/23/2004	METHOD AND APPARATUS FOR POWER MANAGEMENT USING	CONNOLLY,MA

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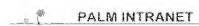
10880331	Not Issued	20050114581	133	06/29/2004	TRANSMISSION MODE WITH REDUCED POWER UNIVERSAL CONTROLLER FOR PERIPHER AL DEVICES IN A COMPUTING SYSTEM	UNELUS,ERNES
10924280	Not Issued	20050114554	124	08/23/2004	PERIPHERAL CONTROLLER WITH SHARED EEPROM	CERULLO,JERE
10945663	Not Issued	20060064755	041	09/21/2004	METHODS AND APPARATUS FOR INTERFACE ADAPTER INTEGRATED VIRUS PROTECTION	POLTORAK,PIO
11561296	Not Issued	20080120531	030	11/17/2006	METHODS AND APPARATUS FOR SOFT DECISION DECODING USING RELIABILITY VALUES BASED ON A LOG BASE TWO FUNCTION	LOUIS JACQUES,JACQI
12187701	Not Issued	0	020	08/07/2008	METHODS AND APPARATUS FOR IMPROVED PHASE LINEARITY IN A MULTI-PHASE BASED CLOCK/TIMING RECOVERY SYSTEM	

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Last Name = HARATSCH First Name = ERICH

Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name
11179715	7274367	20050243092	150	07/12/2005	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	NGUYEN,PHU
11841356	Not Issued	0	071	08/20/2007	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	NGUYEN,PHU
09686077	6483513		150	10/12/2000	METHOD FOR DEFINING MPEP 4 ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	NGUYEN,PHU
09950792	Not Issued	20020075301	161	09/13/2001	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION USING NODES	NGUYEN,KIMBI
11931196	Not Issued	20080114723	030	10/31/2007	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION	GAFFIN,JEFFRE
09975052	Not Issued	0	161	10/12/2001	METHOD FOR DEFINING	NGUYEN,PHU

					ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	
10291428	6970172	20030184547	150	11/12/2002	SYSTEM FOR DEFINING MPEG 4 ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	NGUYEN,PHU
61135732	Not Issued		020		ENHANCED INTER CELL INTERFERENCE CANCELLATION TECHNIQUES FOR FLASH MEMORIES	
11620988	Not Issued	20080168330	025	01/08/2007	SYSTEMS AND METHODS FOR PRIORITIZING ERROR CORRECTION DATA	
12017547	Not Issued	0	025	01/22/2008	METHODS AND APPARATUS FOR REDUCED COMPLEXITY SOFT- OUTPUT VITERBI DETECTION	
11496121	Not Issued	20080052595	030	07/31/2006	METHODS AND APPARATUS FOR LOW-DENSITY PARITY CHECK DECODING USING HARDWARE- SHARING AND SERIAL SUM- PRODUCT ARCHITECTURE	LOUIS JACQUES,JACQI
11535540	Not Issued	20080077829	030	09/27/2006	SYSTEMS AND METHODS FOR GENERATING ERASURE FLAGS	LOUIS JACQUES,JACQI
12017765	Not Issued	0	025	01/22/2008	METHODS AND APPARATUS FOR MAP DETECTION WITH REDUCED COMPLEXITY	

11045585	Not Issued	20060174183	061	01/28/2005	METHOD AND APPARATUS FOR SOFT-OUTPUT VITERBI DETECTION USING A MULTIPLE- STEP TRELLIS	ALPHONSE,FRI
60244453	Not Issued		159	10/30/2000	LOW COMPLEXITY JOINT EQUALIZER AND DECODER FOR 1000BASE-T GIGABIT ETHERNET	
10962188	Not Issued	20050105658	071	10/08/2004	PIPELINED DECISION- FEEDBACK UNIT IN A REDUCED-STATE VITERBI DETECTOR WITH LOCAL FEEDBACK	VLAHOS,SOPHI.
10853087	Not Issued	20050268210	093	05/25/2004	METHOD AND APPARATUS FOR MULTIPLE STEP VITERBI DETECTION WITH LOCAL FEEDBACK	LAMARRE,GUY
10853090	Not Issued	20050264906	161	05/25/2004	METHOD AND APPARATUS FOR REDUCED-STATE VITERBI DETECTION IN A READ CHANNEL OF A MAGNETIC RECORDING SYSTEM	MERCEDES,DIS
10853089	7380199	20050268211	150	05/25/2004	METHOD AND APPARATUS FOR PRECOMPUTATION AND PIPELINED SELECTION OF BRANCH METRICS IN A REDUCED-STATE VITERBI DETECTOR	LAMARRE,GUY
10853088	Not Issued	20050265492	041	05/25/2004	METHOD AND APPARATUS FOR PRECOMPUTATION AND PIPELINED SELECTION OF INTERSYMBOL INTERFERENCE ESTIMATES IN A	GUARINO,RAHE

					REDUCED-STATE	
10022665	Not Issued	20030115541	124	12/18/2001	WITERBI DETECTOR METHOD AND APPARATUS FOR JOINT EQUALIZATION AND DECODING OF MULTIDIMENSIONAL CODES TRANSMITTED OVER MULTIPLE SYMBOL DURATIONS	TORRES,JOSEPH
09834668	7000175	20020083396	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	TORRES, JOSEPI
10022659	Not Issued	20030112885	135	12/18/2001	METHOD AND APPARATUS FOR JOINT EQUALIZATION AND DECODING OF MULTILEVEL CODES	TORRES,JUAN
60245519	Not Issued		159	11/03/2000	PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	
61194751	Not Issued		019	09/30/2008	BELIEF PROPAGATION DECODING OF LDPC CODES FOR FLASH MEMORIES	
61134688	Not Issued		020		INTER CELL INTERFERENCE CANCELLATION TECHNIQUES FOR FLASH MEMORIES	
61133675	Not Issued		020		INTER CELL INTERFERENCE CANCELLATION TECHNIQUES FOR FLASH MEMORIES	
61068934	Not		020	03/11/2008	MULTI-LEVEL	

	Issued				CODING AND READ/WRITE ACCESS STRATEGIES FOR MULTI-PAGE CELL FLASH	
11256182	Not Issued	20060039492	077	10/21/2005	HYBRID MEMORY ARCHITECTURE FOR REDUCED STATE SEQUENCE ESTIMATION (RSSE) TECHNIQUES	PHU,PHUONG
11234446	7363576	20060020877	150	09/26/2005	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	TORRES,JOSEPI
12101322	Not Issued	20080189532	020	04/11/2008	METHOD AND APPARATUS FOR PRECOMPUTATION AND PIPELINED SELECTION OF BRANCH METRICS IN A REDUCED STATE VITERBI DETECTOR	
12039474	Not Issued	0	020	02/28/2008	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	
11691847	Not Issued	20070189424	071	03/27/2007	METHOD AND APPARATUS FOR REDUCED-STATE VITERBI DETECTION IN A READ CHANNEL OF A MAGNETIC RECORDING SYSTEM	MERCEDES,DIS
11119038	Not Issued	20060248435	071	04/29/2005	METHOD AND APPARATUS FOR ITERATIVE ERROR- ERASURE DECODING	TORRES,JOSEPF

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